

Fig. 1
PRIOR ART

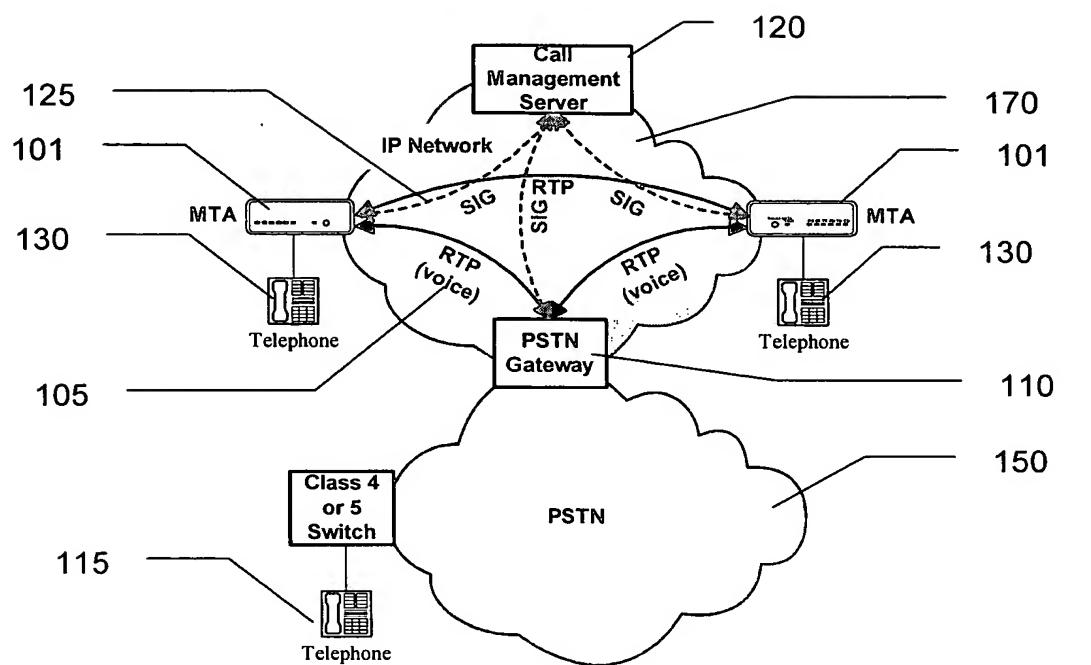
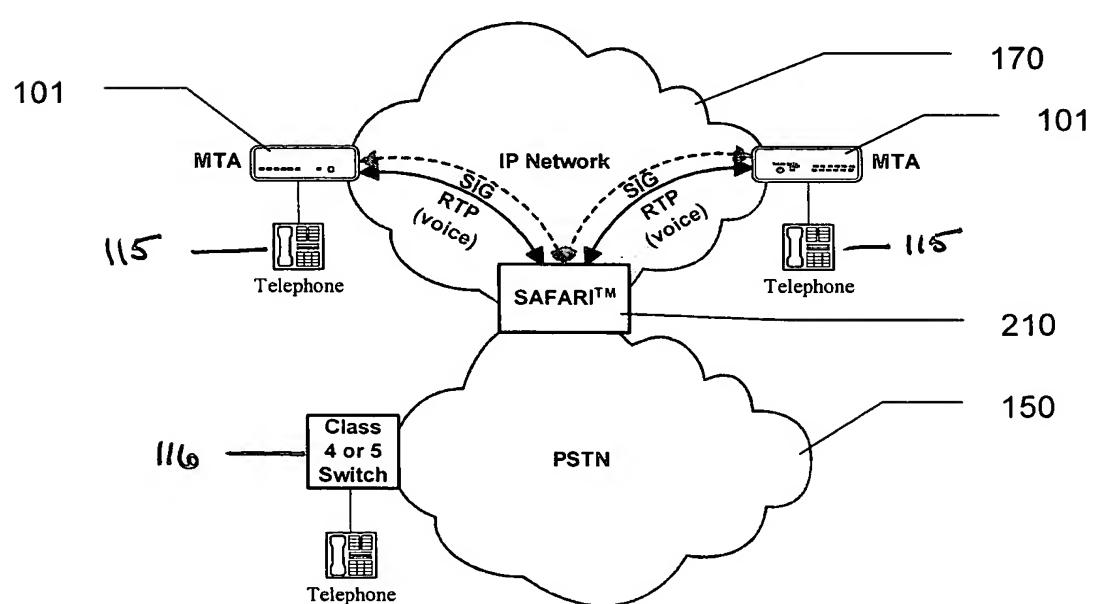


Fig. 2



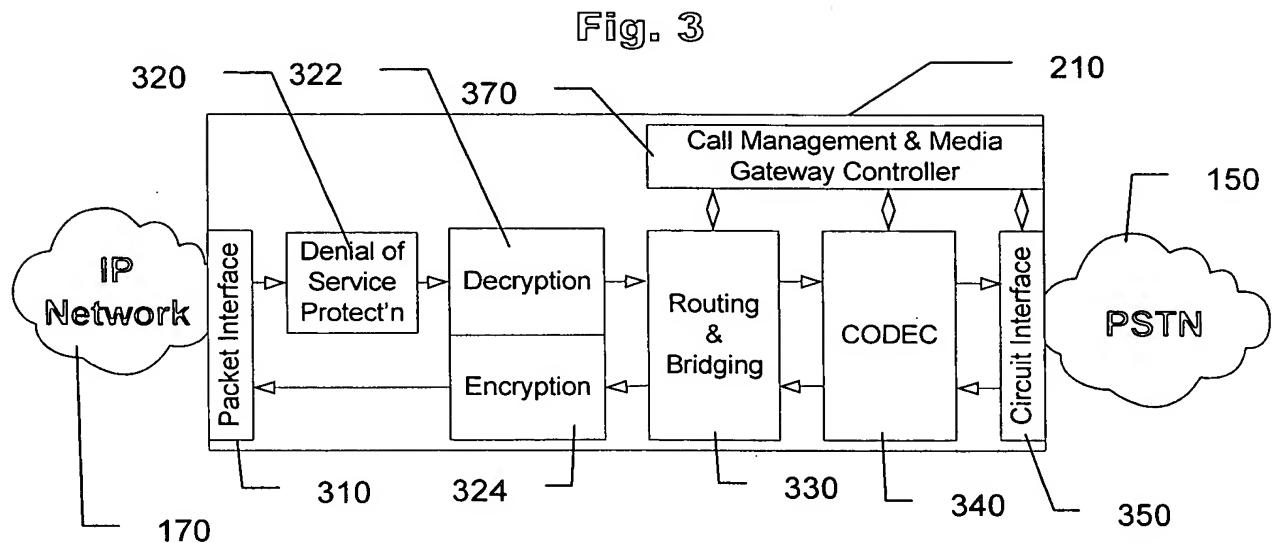


Fig. 4A

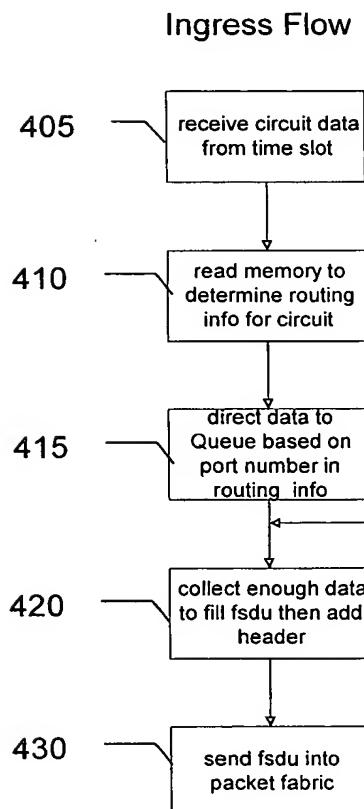


Fig. 4B

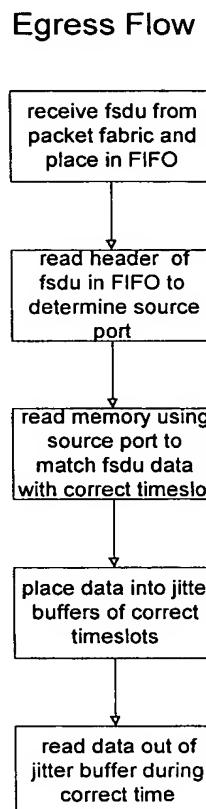
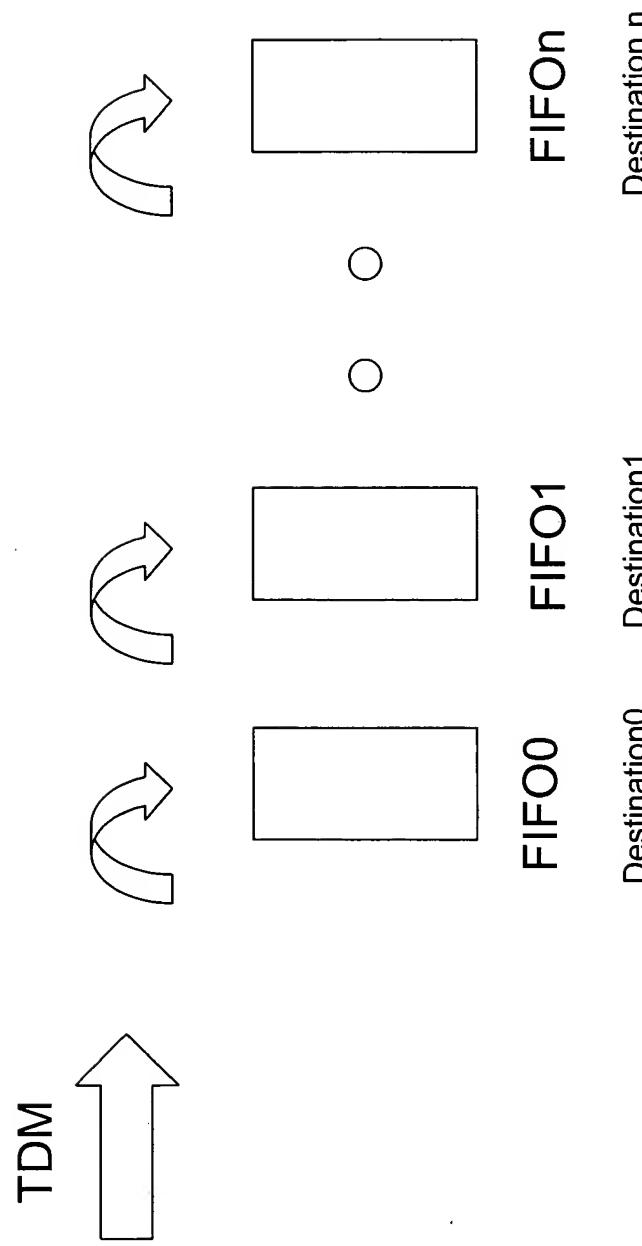


Figure 4C



500

Fig. 5

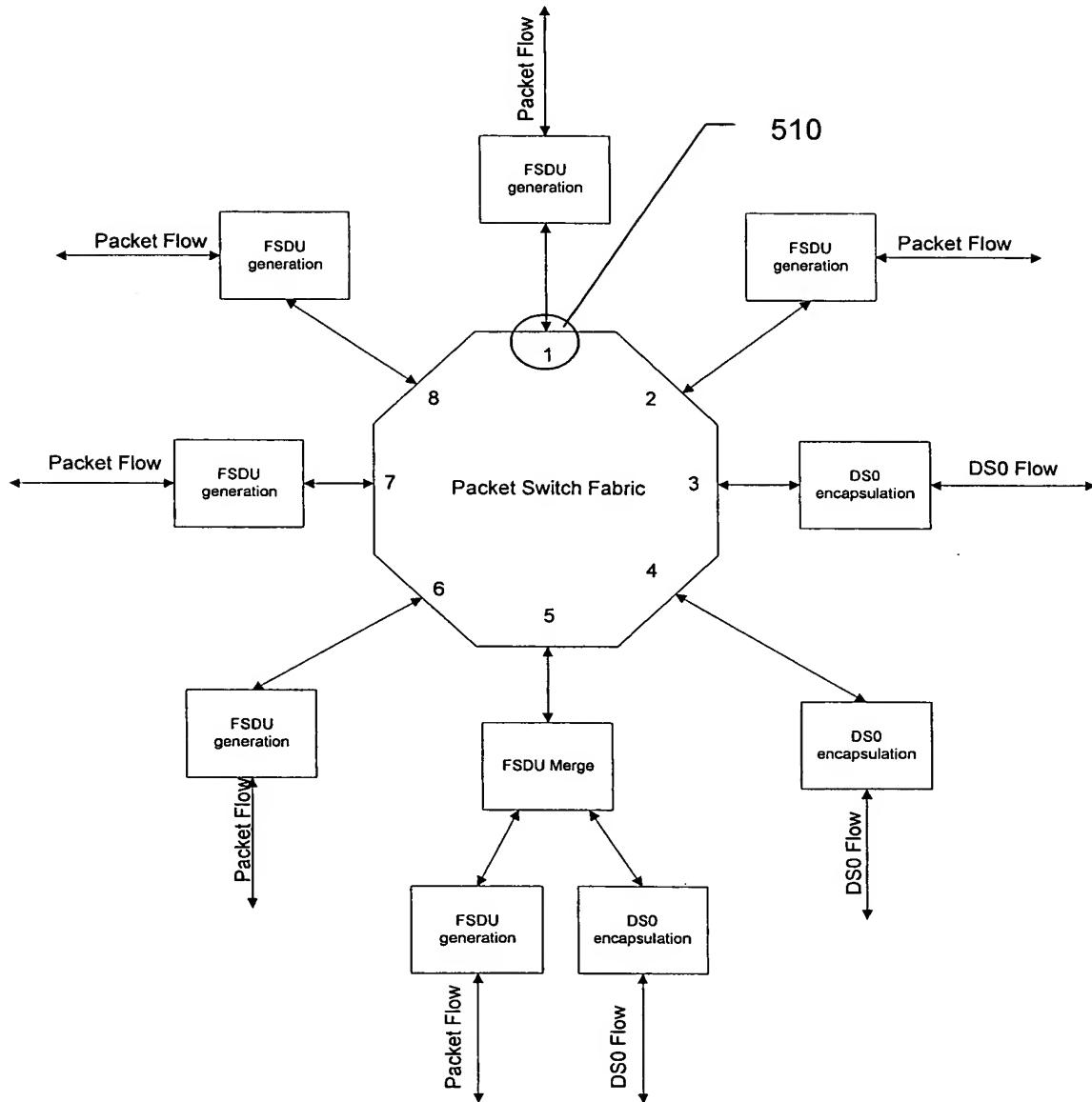


Fig. 6

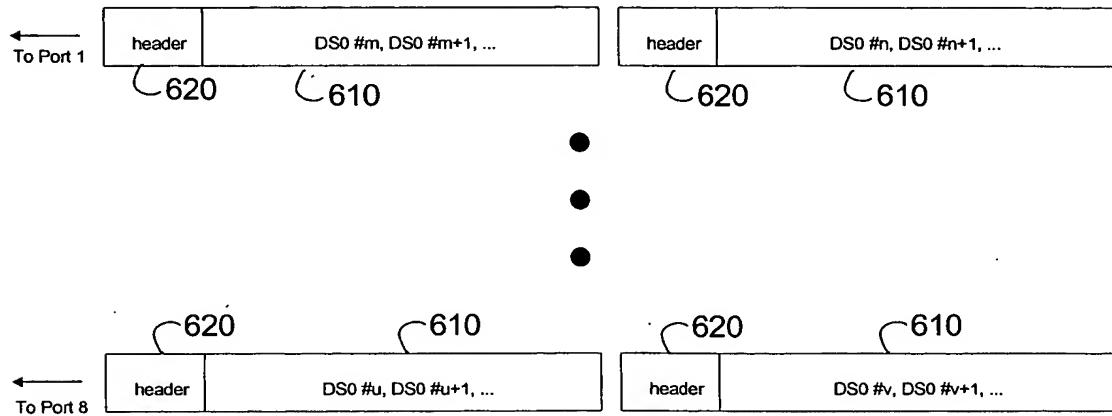
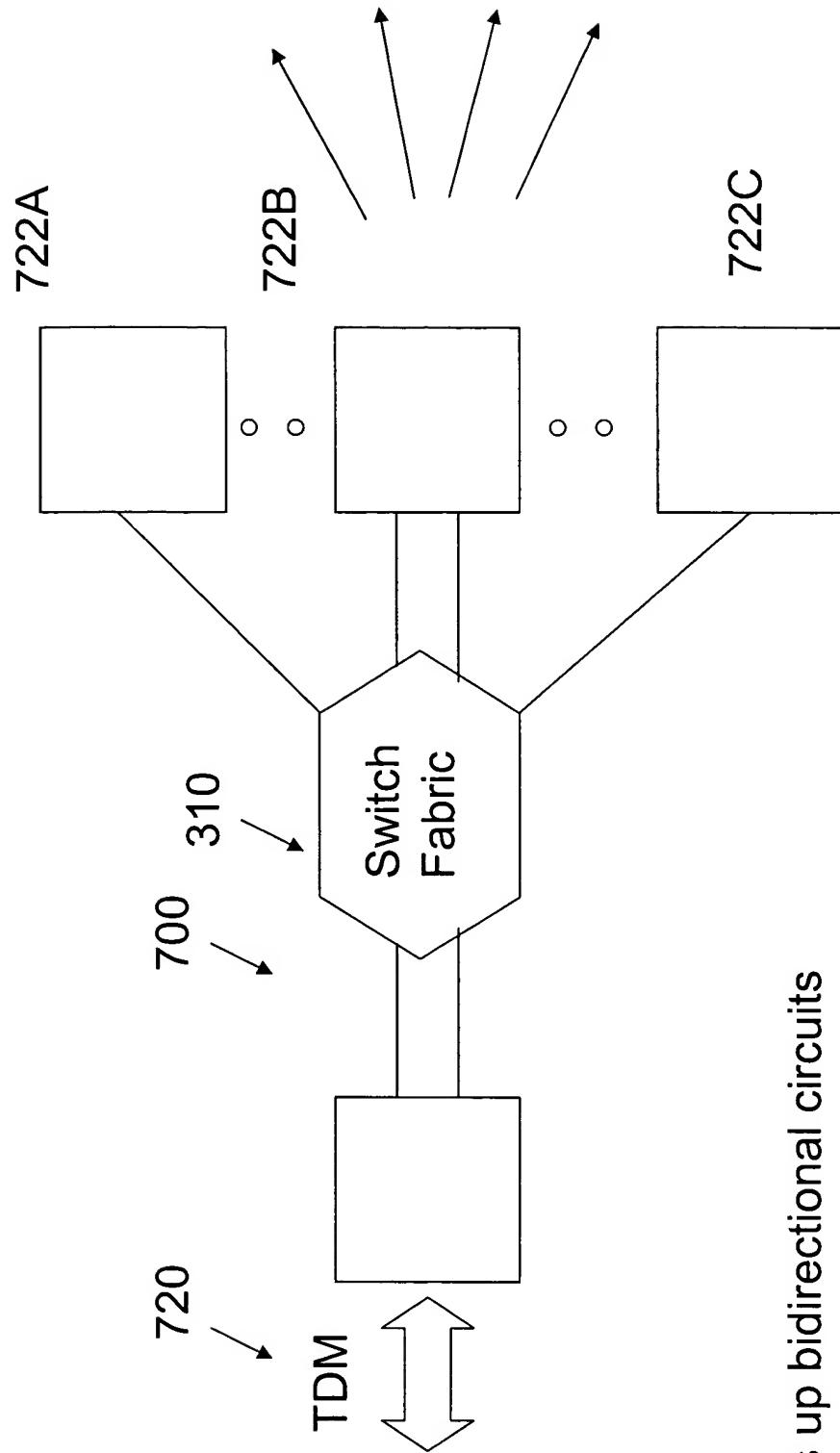


Figure 7



Sets up bidirectional circuits

Figure 8

